



Attorney Docket No. 1076.1023D2

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Hiroko MURAKAMI et al.

Application No.: 10/023,769

ATTENTION

Confirmation No.: 4629

CERTIFICATE OF CORRECTION

Filed: December 21, 2001

BRANCH

U.S. Patent No.: 6,671,112 B2

Batch No.:

Issued: December 30, 2003

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

**Certificate**  
**JAN 29 2004**  
**of Correction**

**REQUEST FOR CERTIFICATE OF CORRECTION**

Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Sir:

Patentee(s) respectfully request(s) that a Certificate of Correction be issued in the subject patent, pursuant to 35 U.S.C. §254 and 37 C.F.R. §1.322, to correct the mistake(s) shown on the attached Form PTO-1050.

Since the mistakes are Patent Office mistakes, it is believed that no fee is required.

Respectfully submitted,

STAAS & HALSEY LLP

Date: January 27, 2003

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JAN 30 2004

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## UNITED STATES PATENT AND TRADEMARK OFFICE

## CERTIFICATE OF CORRECTION

PATENT NO : 6,671,112

DATED : December 30, 2003

INVENTOR(S) : Hiroko MURAKAMI et al.

Title Page

[56] FOREIGN PATENT DOCUMENTS  
insert --JP 3-8173 1/1999--.

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PATENT NO. 6,671,112 B2

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